09/973690   COFC

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent of   :   **Attn: Certificate of**
                                                 Correction Branch

Tsuyoshi YOKOKAWA   :

Patent No. 6,834,034   :   Atty Docket No. 2001_1539A

Issued December 21, 2004   :

                                              Confirmation No. 6318   **Certificate**

OPTICAL DISK DATA ERASING
APPARATUS AND OPTICAL DISK   MAR 1 3 2006
DATA ERASING METHOD   :
                                              of Correction
                                    ‾‾‾‾‾‾‾‾

REQUEST FOR CERTIFICATE OF CORRECTION UNDER 37 CFR 1.323

Commissioner for Patents   THE COMMISSIONER IS AUTHORIZED
   P.O. Box 1450   TO CHARGE ANY DEFICIENCY IN THE
   Alexandria, VA 22313-1450   FEES FOR THIS PAPER TO DEPOSIT
                                               ACCOUNT NO. 23-0975

Sir:

     In accordance with the provisions of 37 CFR 1.323, it is respectfully requested that a

Certificate of Correction issue to correct the following:

In the Specification

     In column 13, line 21, please replace "is to be" with --is not to be--.

     In column 13, line 22, please replace "is not to be" with --is to be--.

M   4 2006

REMARKS

The above amendments are to correct typographical errors in the specification. It is submitted that these amendments do not constitute new matter in light of the disclosure of other portions of the specification as will be discussed below.

The specification indicates that the host computer 21 can execute another processing while the data erasing apparatus 22 is executing the data erasing processing, in the case where the "Immediate" designating command is received, and the host computer 21 can speedily detect the completion of the data erasing processing of the data erasing apparatus 22, in the case where the "Non Immediate" designating command is received. (See column 13, lines 25-55).
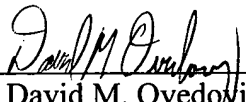
The specification also indicates that when it is set that the interface bus is not to be occupied, the host computer 21 can perform another processing while the data erasing apparatus 22 is executing the data erasing. Further, it is indicated that when the interface bus is to be occupied, the host computer 21 can speedily detect the end of the data erasing by the data erasing apparatus 22. (See column 14, lines 19-26).

Based on these two portions of the specification, it is apparent that when the interface bus is set to not be occupied (i.e., the host computer 21 is capable of performing other processing), the "Immediate" command has been received, and when interface bus is set to be occupied (i.e., the host computer 21 is capable of speedily detecting the completion of data erasing), the "Non Immediate" command has been received. Therefore, the above-mentioned amendments to the specification are clearly not new matter. As a result, the issuance of a Certification of Correction including these amendments to the specification is respectfully requested.

The requisite fee of $100.00 is enclosed herewith.

Respectfully submitted,

Tsuyoshi YOKOKAWA

By _____

David M. Ovedovitz
Registration No. 45,336
Attorney for Patentee

DMO/jmj
Washington, D.C.
Telephone (202) 721-8200
Facsimile (202) 721-8250
March 8, 2006

2006

- 2 -

To: The Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO   :   6,834,034

DATED   :   December 21, 2004

INVENTOR(S)   :   Tsuyoshi YOKOKAWA

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Specification

In column 13, line 21, please replace "is to be" with --is not to be--.

In column 13, line 22, please replace "is not to be" with --is to be--.

Wenderoth, Lind & Ponack, L.L.P.
2033 K Street, N. W., Suite 800
Washington, D.C. 20006-1021

PATENT NO. 6,834,034

No. of additional copies

2006